



PATENT
Attorney's Docket No. H1449

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)	
)	
Wiley Eugene HILL et al.)	Group Art Unit: 2823
)	
Application No.: 10/728,844)	Examiner: W. Brewster
)	
Filed: December 8, 2003)	
)	
For: METHOD OF FORMING)	
MERGED FET INVERTER/LOGIC GATE)	

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

U.S. Patent and Trademark Office
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Randolph Building
401 Dulany Street
Alexandria, VA 22314

Sir:

In response to the Statement of Reasons for Allowance contained in the Notice of

Allowance dated June 15, 2005, Applicants submit the following remarks:

Remarks begin on page 2 of this paper.